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PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: C. Visweswariah

Examiner: Leigh M. Garbowski

Serial No: 10/665,092

Group Art Unit: 2835

Filed: September 18, 2003

Docket: YOR920030401US1 (163-127)

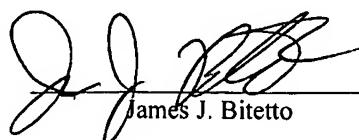
For: SYSTEM AND METHOD FOR INCREMENTAL STATISTICAL TIMING ANALYSIS
OF DIGITAL CIRCUITSMail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**AMENDMENT UNDER 37 C.F.R. §1.111**

Sir:

This is a response to the Office Action mailed January 26, 2006. Please amend the above-identified application as follows:

CERTIFICATE OF MAILING 37 C.F.R. §1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 4/26/06.


James J. Bitetto